



Gastreferat

Mittwoch, 30. September 2020
17:10 Uhr im Gebäude 4, Raum 4.006a

PULP Platform

Open source RISC-V based energy efficient computing solutions

Referent:

Frank K. Gürkaynak

In this talk, the PULP platform will be introduced, which is a project by ETH Zurich and University of Bologna under the guidance of Prof. Luca Benini since 2013. In this short time, already 37 ASICs were taped out with PULP technology. It has provided the backbone of many collaborative projects and it has been even used in commercial products. The status and next goals of the project will be explained and the different collaboration models that have been used so far will be discussed.

Referent

Frank K. Gürkaynak is the director of the Microelectronics Design Center that supports IC, FPGA and PCB design within ETH Zurich and he works as a Senior Scientist in the group of Luca Benini supporting him in running the group. He has been part of PULP project from its beginning.

