

Gastreferat Microelectronics

Studiengang Elektrotechnik



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PULP Platform

Open source RISC-V based energy
efficient computing solutions

Referent: **Frank K. Gürkaynak**

In this talk, the PULP platform will be introduced, which has been a project by ETH Zurich and the University of Bologna under the guidance of Prof. Luca Benini since 2013. In this short time, 50 ASICs have already been taped out with PULP technology. It has provided the backbone of many collaborative projects and it has even been used in commercial products. The status and next goals of the project will be explained and the different collaboration models that have been used so far will be discussed.



Frank K. Gürkaynak is the director of the Microelectronics Design Center which supports IC, FPGA and PCB design within ETH Zurich and he works as a Senior Scientist in the group of Luca Benini supporting him in running the group. He has been part of PULP project since its beginning.

Veranstaltungsort

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